



## IIR FILTER IN ALL DIGITAL PHASE LOCKED LOOP

Anupama.Patil<sup>1</sup>, P .H Tandel<sup>2</sup>

Pacific Academy of Higher Education and Research Udaipur, Rajasthan

Email:anubnad@gmail.com<sup>1</sup>,phtandel@yahoo.com<sup>2</sup>

### Abstract

**An All Digital Phase locked loop (ADPLL) nowadays is gaining importance in communication systems .It is one of the key building blocks of modern electronic designs. This paper presents a an ADPLL structure that utilizes a hilbert transformer as phase detector an IIR filter as its loop filter, and a NCO as DCO. The advantage of IIR filter has been highlighted in this paper as concerned to Phase Locked Loop otherwise FIR is normally chosen. This All-digital PLL (ADPLL) is found to achieve the desired performance and functionality with advantages that it does not include R and C components.It has been designed to meet the needs of Indian Navy applications of tracking the moving target. Its ease of implementation with required stability using digital CMOS process. This paper presents detailed descriptions of each block of this ADPLL.**

**Index Terms: IIR filter Hilbert PD, Navy, ADPLL**

### I. INTRODUCTION

Order to have increased performance, speed, reliability, and and also reduction in *size* and cost of integrated circuits has resulted in research of the implementation of control and communication systems in the digital domain. A digital version of the phase-locked loop solves the problems associated with its analogue counterpart like sensitivity to DC drifts and component saturations, difficulties that one faces in building higher order loops .No necessity occurs for initial calibration and periodic adjustments. In real time processing on

the signal samples, the DPLLs are more flexible and versatile.

The earliest efforts on DPLLs concentrated on partially replacing the analogue PLL (APLL) components with digital ones. The first all DPLL was reported by Drogin [1] in 1967. Some other authors [2—4] have suggested many kinds of all digital phase-locked loops and have discussed various aspects of implementing them

The first ADPLL consisting of digital components was reported in 1980 [8]. control and communication systems. However, due to the advances in integrated circuit (IC) fabrication and the growth in improving the overall system Digital PLL The important drawbacks of analog PLL are low operating speed, larger chip area, worse jitter performance and high power consumption [9][11]. These are overcome by Digital PLL where the analog phase detector is replaced by its digital counterpart. Digital PLL allows faster lock time to be achieved and generates a clock signal for high performance microprocessor [10]. All Digital PLL is a modified form of Digital PLL. It consists of only Digital blocks. All Digital PLL provides high performance, better Noise immunity and flexibility of operation,

### A Basic Architecture of an All-Digital PLL

The architecture of an ADPLL is same as an analog PLL. The purpose of a PLL is same whether its digital or analog. A time to digital converter is introduced in place of the phase frequency detector and charge pump. The ADPLL is shown in Figure 1.1

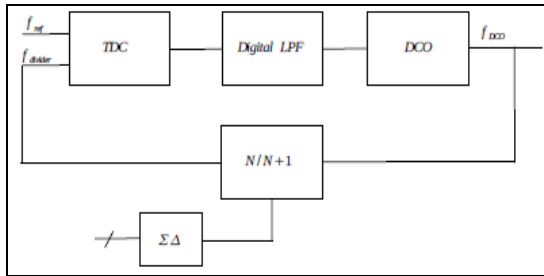


Figure 1.1 Basic architecture of ADPLL

### B Components of All Digital Phase Locked Loop

ALL digital Phase detectors can be of the following type

- i. EXOR gate phase detector
- ii. JK Flip-flop phase detector
- iii. 3Digital phase frequency detector
- iv. NRPD –Nyquist rate phase detector
- v. Hilbert transform phase detector

Mostly used Loop Filters are of

- i. UP/Down counter loop filter
- ii. K counter loop filter
- iii. Digital Filters can be derived from transfer functions

The following are types of digitally controlled oscillators

- i. Divide by N counters
- ii. Increment decrement type
- iii. Accumulator type dco

The PD was the only component that was digitized long back. It's used in the Digital PLL. Similar idea can be extended to the ADPLL. The three common implementations of the digital PD are:

1. Exclusive-or (EXOR) Gate
2. Edge triggered JK Flip-Flop
3. Digital Phase-Frequency Detector

## II Designed ADPLL for Navy application

In our design we have utilized Hilbert transform phase detector, IIR low pass filter of second order and Numerically controlled oscillator (NCO)

### 2.1 Hilbert Transform Phase detector:

The Hilbert Transform based Phase Detector (HTPD). It uses quadrature signal processing method, estimates phase difference between input and output signals without using LP filter.

The block diagram of Hilbert Phase detector used in this research paper is shown in figure 2.1.

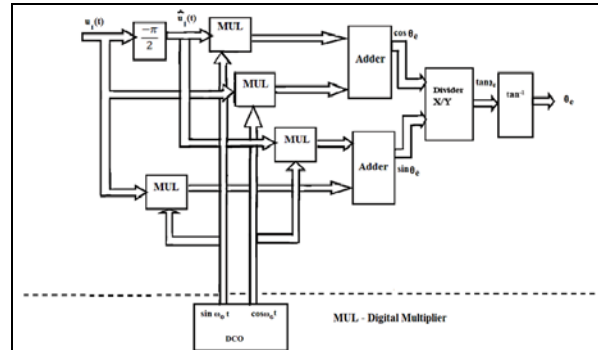


Figure 2.1 Block diagram of Hilbert transformer phase detector [12]

### 2.2 Digital Loop filter

A digital filter is a basic building block in any digital system and also All Digital Phase Locked Loop. Benefit of replacing the bulky passive loop filter by a digital filter is its cost effectiveness and flexibility. The frequency response of the digital filter depends on the value of its coefficients [12]. The values of the coefficients are computed based on the desired frequency response. These values are typically floating point numbers and they are represented with a fairly high degree of precision [11].

While implementing a digital filter, the coefficients need to be represented with the smallest number of bits retaining an acceptable resolution for the numbers. The reason behind this is, representing a number with excess bits increases the size of the registers, buses, adders and multipliers. The bigger sizes of implemented circuits result into a chip with a larger size, which results in increased power consumption. Therefore, the bit precisions are important in the performance of desired digital filter.

A digital filter is categorized into two classes known as

1. A finite impulse response (FIR) filter
- An infinite impulse response (IIR) filter

As the terminology suggests, these classifications refer to the filter's impulse response. By varying the weight of the coefficients and the number of filter taps. Virtually any frequency response characteristic can be realized with an FIR filter. It is seen that

FIR filters can achieve performance levels which are not possible with analog filter techniques (e.g. perfect linear phase response). Disadvantage is that high performance FIR filters require a large number of multiply-accumulates. IIR filters go on the lines of traditional analog filters and make use of feedback.

The digital loop filter (LF) required as a block of Digital Phase Locked Loop is nothing but a PI-controller that operates in discrete time. The LF controls the Normalized Tuning Word (NTW) depending on the phase error ( $\Phi$ ) is the phase difference between the reference phase and the output phase.

**A Properties of Infinite Impulse Response Filters**

- Infinite Impulse Response Filters Uses Feedback (Recursion)
- Impulse Response has an Infinite Duration
- Potentially Unstable
- Non-Linear Phase
- More Efficient than FIR Filters
- No Computational Advantage when Decimating Output
- Usually Designed to Duplicate Analog Filter Response
- Usually Implemented as Cascaded Second-Order Sections (Biquads)

These digital filters closely resemble the analog filters in characteristics and properties. IIR filters have a feedback and a feed forward path. Both the feedback and the feed forward path contribute to the output sample calculation. The feed forward part of the IIR's is like simple FIR filter. The feedback and feed forward paths should counterpoise each other. To generate an output sample the previous input is added to the new input. The previous output is then subtracted from this sum of inputs. Then this whole calculated sum is multiplied by the coefficients. An IIR filter of order  $N$  requires  $2N+1$  coefficients for its transfer function. From hardware perspective it requires  $2N+1$  multipliers and  $2N$  adders. The coefficients are chosen based on the frequency specifications of the required filter. Some of the basic structures/realizations of the digital IIR filters are as discussed below:

**A Direct Form IIR Digital Filter**

In direct form IIR filters the transfer function of

the filter has the same coefficients as the multipliers. The general form of the transfer function of a  $n$ th-order recursive filter

$$\frac{y_n}{x_n} = \frac{a_0 + a_1 z^{-1} + \dots + a_n z^{-n}}{b_0 + b_1 z^{-1} + \dots + b_n z^{-n}} \quad (1)$$

Example of the transfer function for the third order IIR filter is given as:

$$H(z) = \frac{B(z)}{A(z)} = \frac{b_0 + b_1 z^{-1} + b_2 z^{-2} + b_3 z^{-3}}{(1 + a_1 z^{-1} + a_2 z^{-2} + a_3 z^{-3})} \quad (2)$$

Transfer function given in equation (1) can be implemented as in figure 2.2.

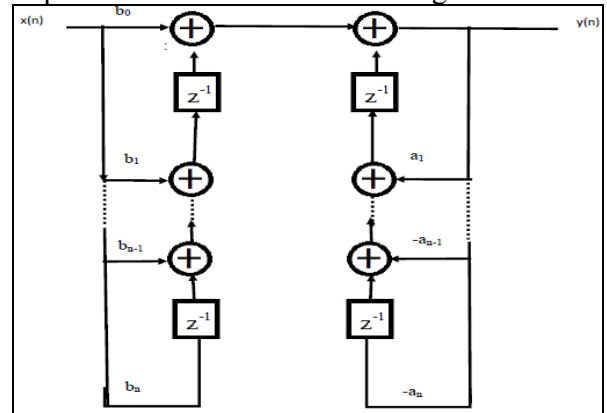


Figure 2.2 IIR filter direct realisation

**B Transpose Form IIR Digital Filter**

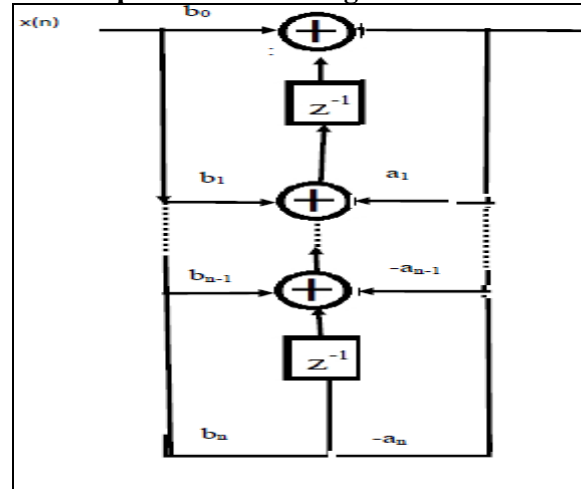


Figure 2.3 IIR filter transpose realization

In transpose realization delay elements are clubbed together to reduce the number of computations and hence increase the speed of operation.

In the ADPLL we have designed in order to design third order ADPLL we make use of

second order IIR filter whose characteristics are shown in figure .The filter and other blocks are implemented in matlab and simulations are also presented.

**C Advantages of IIR filter**

1. IIR is infinite and used for applications where linear characteristics are not of concern.
2. IIR is better for lower-order tapping, whereas the FIR filter is used for higher-order tapping.
3. IIR filters are recursive and used as an alternate, whereas

FIR filters have become too long and cause problems in various applications.

**2.3 Digitally controlled Oscillator**

The DCO is the heart of the ADPLL, as it converts the tuning word into the output frequency.It is also known as numerically controlled oscillator(NCO). NCOs can be used to generate a wide variety of periodic output waveforms, but for the purposes of this paper a cosine output function is assumed.

$$y(n)=\cos(\phi(n)) \tag{3}$$

For a fixed output frequency of  $f_0$  cycles per sample, and a fixed output phase of  $\theta_0$  radian,  $\phi(n) = 2\pi f_0(n) + \theta_0$  giving

$$y(n)=\cos(2\pi f_0(n)+\theta_0) \tag{4}$$

If the frequency and phase are changing with  $n$ , the instantaneous frequency  $f_i(n)$  provides the increment for  $\phi(n)$  which must be accumulated over time, while the instantaneous phase offset  $\phi_i(n)$  provides an offset value to be applied only to the  $n$ th sample.

$$\phi_i(n) = \phi_i(n-1) + 2\pi f_i(n) \quad \phi(n) = \phi_i(n) + \theta_i(n) \tag{5}$$

An NCO consists of two fundamental blocks: a digital “phase accumulator” to perform the calculation of (5),

and a “phase-to-amplitude converter” which converts the values of  $\phi(n)$  to create the output sample values given by (1).

The NCO structure is illustrated in Figure 2.4

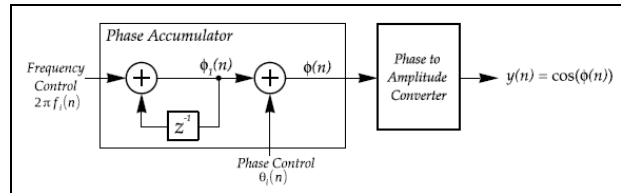


Figure 2.4 Structure of Numerically controlled oscillator

**3.Implementation of ADPLL using IIR Filter in Matlab**

IIR Filter is designed with cutoff of around 1.1 Mhz.Centre frequency of ADPLL is 250 Mhz.The matlab simulink model of filter and complete ADPLL realization figures are as shown in figures below.

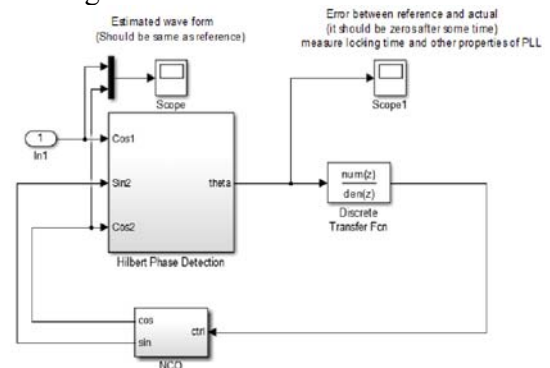


Figure 3.1 ADPLL realization using matlab simulink

The Low pass filter used has the transfer function

$$\frac{z - 0.9947}{z^2 - 1.97z + 0.9696} \tag{6}$$

The bode plot is as in figure 3.2

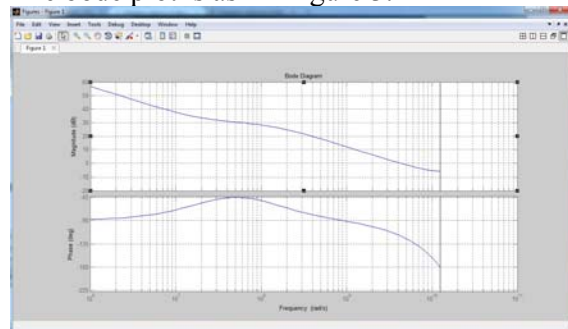


Figure 3.2 Frequency response of IIR filter utilized in the ADPLL.

It has been found to track the signal after connecting with Hilbert transform Phase detector and the NCO as shown in the matlab simulink model perfectly.

The phase locking response as found by the designed ADPLL is as given in figure 3.4

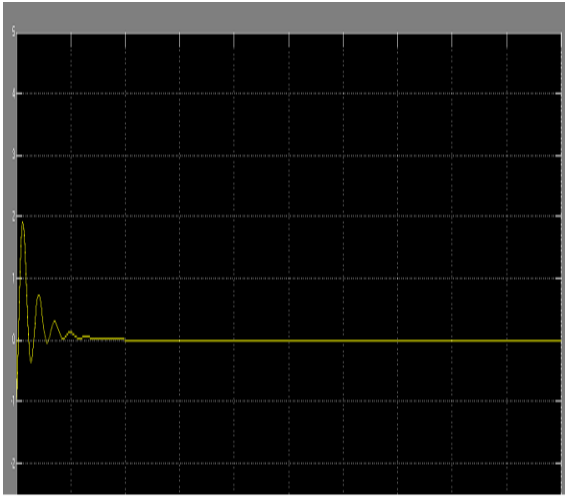


Figure 3.4 ADPLL response phase has been locked with very long locking range.

So we have achieved the locking successfully with practically infinite locking range using the second order IIR filter and thus realized the third order ADPLL

#### 4. Application of the designed ADPLL.

I propose to use the ADPLL with center frequency of 250 MHz and Low pass filter cut off of approximately 1.2-1.5 MHz for the moving target tracking in Indian navy where UHF range is usually used for the tracking. Naval communication systems vary in complexity depending upon their role, compatibility, and flexibility. Due to scarcity of space on board a ship, the communication equipment is spread across the ship's compartments; however, it is ensured that the sets are capable of operating separately as well as concurrently. Complex interconnections provide the ability of selectively switching different configurations.

Radiofrequency bands commonly used for naval communication include, very high frequency and above, high frequencies, medium frequency, low frequency, very low frequency, and extremely low frequency.

Very High Frequency and above (30 MHz – 300 MHz) are only used for line of sight communication as ground range is very less. Radar is an electromagnetic (EM)

sensor system used for detection, location tracking, imaging and classification of targets such as man-made objects like aircraft, ships, ground moving vehicles and natural environment including ground features and moving men. It is an important sensor for the

commanders' battle space awareness. Modern battle has transformed into network centric with unified battlefields spread across multi theatres of operation. With the advent of microelectronics, radio frequency (RF) management and unprecedented growth in processing power and computation, radar has transformed into a complex, advanced and intelligent sensor. Indian military, aspiring to be a global player, has been integrating emerging technologies to fine tune its strategies and tactics to integrate with global Armed Forces[.3]

#### 4. Conclusion

It has been found that in spite of feedback paths IIR filters still have advantage which has been verified using the ADPLL design for 250 MHz. It has been seen to track faster than its counterparts with EXOR or JK flip, flop Phase detectors. Also further attempts and sincere efforts are being made to employ this in Indian Navy for tracking the accelerating moving targets. The main purpose of using the third order PLL is to track both horizontally and vertically the accelerating target which is quite a normal occurrence and difficult practically in Navy.

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